



Time Table Issue Date: 09.08.2018

**ADMITTED BATCH - 2017-18, BR-16 Regulation**  
**II B.TECH - I Semester - I MID TERM EXAMINATIONS, August - 2018**  
**TIME TABLE**

TIME OF EXAM : FN : 10:00AM TO 11:30 AM

AN: 1:30 PM to 3:00 PM

S.N	BRANCH	27.08.2018 (FN)	27.08.2018 (AN)	28.08.2018 (FN)	28.08.2018 (AN)	29.08.2018 (FN)	29.08.2018 (AN)
1	CIVIL	MATHEMATICS-II R16MTH1102	STRENGTH OF MATERIALS-I R16CIV1101	SURVEYING R16CIV1102	FLUID MECHANICS R16CIV1103	ELECTRICAL & ELECTRONICS ENGINEERING R16EEE1131	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS R16HAS1103
2	EEE	MATHEMATICS-III R16MTH1104	FLUID MECHANICS AND HYDRAULIC MACHINERY R16MED1137	ELECTRONIC DEVICES AND CIRCUITS R16ECE1102	ELECTRICAL CIRCUITS R16EEE1101	ELECTRO MAGNETIC FIELDS R16EEE1102	ELECTRICAL MACHINES-I R16EEE1103
3	MECH	METALLURGY AND MATERIALS SCIENCE R16MED1106	PROBABILITY AND STATISTICS R16MTH1105	THERMODYNAMICS R16MED1105	MECHANICS OF SOLIDS R16MED1104	ELECTRICAL & ELECTRONICS ENGINEERING R16EEE1131	ENVIRONMENTAL STUDIES R16HAS1102
4	ECE	MATHEMATICS-III R16MTH1104	PROBABILITY THEORY AND STOCHASTIC PROCESSES R16ECE1101	ELECTRONIC DEVICES AND CIRCUITS R16ECE1102	ELECTRICAL CIRCUITS R16EEE1101	SWITCHING THEORY AND LOGIC DESIGN R16ECE1106	SIGNALS AND SYSTEMS R16ECE1103
5	CSE	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE R16CSE1103	PROBABILITY AND STATISTICS R16MTH1105	ELECTRONIC DEVICES AND CIRCUITS R16ECE1102	DATA STRUCTURES THROUGH C++ R16CSE1104	BASIC ELECTRICAL ENGINEERING R16EEE1130	DIGITAL LOGIC DESIGN R16ECE1135
6	IT	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE R16CSE1103	PROBABILITY AND STATISTICS R16MTH1105	ELECTRONIC DEVICES AND CIRCUITS R16ECE1102	DATA STRUCTURES THROUGH C++ R16CSE1104	BASIC ELECTRICAL ENGINEERING R16EEE1130	DIGITAL LOGIC DESIGN AND COMPUTER ORGANIZATION R16INF1102

NOTE: (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

NOTE: (ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAMINATION.

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 CE 9/8/18  
**CONTROLLER OF EXAMINATIONS**  
 Sri Indu College of Engineering & Technology  
 (An Autonomous Institution under JNTUH)  
 Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510

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**DEAN**  
**DEAN, ACADEMIC AFFAIRS & EVALUATION**  
 Sri Indu College of Engineering & Technology  
 (An Autonomous Institution under JNTUH)  
 Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510

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**PRINCIPAL**  
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