



REVISED - II B.TECH - II Semester - (Suppl.) END EXAMINATIONS, December - 2018
(For Admitted Batch - 2016-17, BR-16 Regulation)
TIME TABLE

Time of Exam: 10:00 AM to 1:00 PM

Sl.No	BRANCH	12.12.2018 10:00 AM - 1:00 PM	13.12.2018 10:00 AM - 1:00 PM	14.12.2018 10:00 AM - 1:00 PM	15.12.2018 10:00 AM - 1:00 PM	17.12.2018 10:00 AM - 1:00 PM	18.12.2018 10:00 AM - 1:00 PM
1	CIVIL	Probability & Statistics R16MTH1105	Strength of Materials – II R16CIV 1104	Hydraulics & Hydraulic Machinery R16CIV 1105	Environmental Studies R16HAS1102	Structural Analysis – I R16CIV 1106	Building Materials, Construction & Planning R16CIV 1107
2	EEE	Managerial Economics & Financial Analysis R16HAS1103	Power Systems – I R16EEE1104	Electronic Circuits R16ECE1135	Switching Theory and Logic Design R16ECE1106	Network Theory R16EEE1105	Electrical Machines – II R16EEE1106
3	MECH	Production Technology R16MED1107	Kinematics of Machinery R16MED1108	Thermal Engineering – I R16MED1109	Mechanics of Fluids and Hydraulic Machines R16MED1110	Machine Drawing R16MED1111	Mathematics – II R16MTH1107
4	ECE	Principles of Electrical Engineering R16EEE1129	Electronic Circuit Analysis R16ECE1104	Pulse and Digital Circuits R16ECE1105	Environmental Studies R16HAS1102	Electromagnetic Theory and Transmission Lines R16ECE1107	Digital Design using Verilog HDL R16ECE1132
5	CSE	Computer Organization R16CSE1105	Database Management Systems R16CSE1106	Java Programming R16CSE1107	Environmental studies R16HAS1102	Formal Languages and Automata Theory R16CSE1108	Design and Analysis of Algorithms R16CSE1109
6	IT	Principles of Programming Languages R16CSE1110	Database Management Systems R16CSE1106	Java Programming R16CSE1107	Environmental Studies R16HAS1102	Data Communication R16INF1101	Design and Analysis of Algorithms R16CSE1109

NOTE: (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

NOTE: (ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAMINATION.

CONTROLLER OF EXAMINATIONS
 Sri Indu College of Engineering & Technology
 (An Autonomous Institution under JNTUH)
 Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510.

DEAN
DEAN, ACADEMIC AFFAIRS & EVALUATION
 Sri Indu College of Engineering & Technology
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 Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510

PRINCIPAL
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