



SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY

(An Autonomous Institution under UGC, New Delhi)

Recognized under 2(f) and 12(B) of UGC Act 1956

NBA Accredited, Approved by AICTE and Permanently affiliated to JNTUH

Sheriguda (V), Ibrahimpatnam, R.R.Dist, Hyderabad - 501 510

D4

BR- 16

Time Table Issue Date: 25.01.2019

ADMITTED BATCH : 2016-17, BR-16 Regulation

III B.TECH - II SEMESTER - I MID TERM EXAMINATIONS, February - 2019

TIME TABLE

TIME OF EXAM :- FN- 10:00AM TO 11:30 AM

AN 1:30PM TO 3:00PM

SL. NO	BRANCH	14.02.2019 (FN)	14.02.2019 (AN)	15.02.2019 (FN)	15.02.2019 (AN)	16.02.2019 (FN)	16.02.2019 (AN)
1	CIVIL	STEEL STRUCTURES DESIGN AND DRAWING R16CIV1116	ENVIRONMENTAL ENGINEERING R16CIV1117	TRANSPORTATION ENGINEERING-I R16CIV1119	FOUNDATION ENGINEERING R16CIV1122	STRUCTURAL ANALYSIS-II R16CIV1115	GROUND WATER HYDROLOGY R16CIV1124
2	EEE	ELECTRICAL AND ELECTRONICS INSTRUMENTATION R16EEE1111	STATIC DRIVES R16EEE1112	COMPUTER METHODS IN POWER SYSTEMS R16EEE1113	MICROPROCESSORS AND INTERFACING DEVICES R16ECE1118	DISASTER MANAGEMENT R16CIV1132	ENVIRONMENTAL STUDIES R16HAS1102
3	MECH	AUTOMOBILE ENGINEERING R16MED1124	FINITE ELEMENT METHODS R16MED1118	REFRIGERATION AND AIR CONDITIONING R16MED1119	DESIGN OF MACHINE MEMBERS -II R16MED1120	HEAT TRANSFER R16MED1121	INDUSTRIAL MANAGEMENT R16MED1117
4	ECE	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS R16HAS1103	TELEVISION ENGINEERING R16ECE1122	DIGITAL COMMUNICATIONS R16ECE1112	VLSI DESIGN R16ECE1115	MICROPROCESSORS AND MICROCONTROLLERS R16ECE1113	DIGITAL SIGNAL PROCESSING R16ECE1114
5	CSE	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS R16HAS1103	SOFTWARE TESTING METHODOLOGIES R16CSE1121	OBJECT ORIENTED ANALYSIS AND DESIGN R16CSE1116	DISTRIBUTED SYSTEMS R16CSE1143	INFORMATION SECURITY R16CSE1117	WEB TECHNOLOGIES R16CSE1119
6	IT	CLOUD COMPUTING R16CSE1125	SOFTWARE TESTING METHODOLOGIES R16CSE1121	OBJECT ORIENTED ANALYSIS AND DESIGN R16CSE1116	DATA WAREHOUSING AND DATA MINING R16CSE1122	DISASTER MANAGEMENT R16CIV1132	WEB TECHNOLOGIES R16CSE1119

NOTE: (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

NOTE: (ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAMINATION.

ACE

CE 25/1/19
CONTROLLER OF EXAMINATIONS
Sri Indu College of Engineering & Technology
(An Autonomous Institution under JNTUH)
Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510

DEAN, ACADEMIC AFFAIRS & EVALUATION
Sri Indu College of Engineering & Technology
(An Autonomous Institution under JNTUH)
Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510

PRINCIPAL
Sri Indu College of Engineering & Technology
(An Autonomous Institution under JNTUH)
Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510