SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY
(An Autonomous Institution under UGC, New Delhi)
Recognized under 2(f) and 12(B) of UGC Act 1956
NBA Accredited, Approved by AICTE and Permanently affiliated to JNTUH
Sheriguda (V), Ibrahimpatnam, R.R.Dist, Hyderabad - 501 510

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MR-20

Time Table Issue Date: 12.02.2022

TIME TABLE (OFF-LINE MODE)
M.TECH I YEAR - I SEMESTER - MID - I EXAMINATIONS - FEB-2022
ADMITTED BATCH - 2021-22 (MR-20 REGULATION)
(BRANCH :- EMBEDDED SYSTEMS)

TIME OF EXAMINATION: FN - 10:00 AM TO 12:00 PM

SL.NO	DATE	SUBJECT
1	16.02.2022	Micro Controllers And Programmable Digital Signal Processors (R20ES1501)
2	17.02.2022	System Design With Embedded Linux (R20ES1502)
3	18.02.2022	Programming Languages For Embedded Software (R20ES1503)
4	19.02.2022	Communications Buses And Interfaces (R20ES1506)
5	21.02.2022	Research Methodology And IPR (R20ES1509)

NOTE :

(i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

(ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED AFTER THE LAST DAY OF THE EXAMINATION.

(iii) THE EXAMINATIONS WILL BE CONDUCTED OFF-LINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

ACE

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DEAN

PRINCIPAL

CONTROLLER OF EXAMINATIONS
Sri Indu College of Engineering & Technology
(An Autonomous Institution under JNTUH)
Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510

DIRECTOR
(Academic Audit)
Sri Indu College of Engineering & Technology
Sheriguda, IBP, R.R. Dist-501510.

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