

SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY

(An Autonomous Institution under UGC, New Delhi)

Recognized under 2(f) and 12(B) of UGC Act 1956

NBA Accredited, Approved by AICTE and Permanently affiliated to JNTUH Sheriguda (V), Ibrahimpatnam, R.R.Dist, Hyderabad - 501 510

D4 MR-20

Time Table Issue Date: 18.06.2022

TIME TABLE (OFF-LINE MODE)

M.TECH I YEAR - II SEMESTER - MID - I EXAMINATIONS - JUNE-2022 ADMITTED BATCH - 2021-22 (MR-20 REGULATION) (BRANCH :- EMBEDDED SYSTEMS)

TIME OF EXAMINATION: FN - 10:00 AM TO 12:00 PM

SL.NO	DATE	SUBJECT
1	27.06.2022	RTL Simulation and Synthesis with PLDs (R20ES1510)
2	28.06.2022	Advanced Digital Signal Processing (R20ES1511)
3	29.06.2022	IOT and its Applications (R20ES1512)
4	30.06.2022	Hardware and Software Co-Design (R20ES1515)

NOTE:

(i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

(ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED AFTER THE LAST DAY OF THE EXAMINATION.

(iii) THE EXAMINATIONS WILL BE CONDUCTED OFF-LINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

CONTROLLER OF EXAMINATIONS

Srl Inua College of Engineering & Technology (An Autonomous Institution under JNTUH)

Sherigude (V), Ibrahimpatham, R.R.Dist.-501510 Sri Indu College of Engineering & Technology

DIRECTOR

DIRECTOR

(Academic Audit)

Sri Indu College of Engineering & Technology (An Autonomous Institution under JNTUH) Sheriguda (V), Ibrahimpatnam, R.R.Dist.-501510.

Sheriguda, IBP, R.R. Dist-501510.