



2001

SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY

(An Autonomous Institution under UGC, New Delhi)

Recognized under 2(f) and 12(B) of UGC Act 1956

NBA Accredited, Approved by AICTE and Permanently affiliated to JNTUH

Sheriguda (V), Ibrahimpatnam, R.R.Dist, Hyderabad - 501 510

D4

BR-18

Lr.No. SICET/DAE/AUTO/III&II-II B.Tech (BR-18)-RMT/ 304/2022.

Dt: 26/07/2022

III-II & II-II B.TECH REMEDIAL MID TEST
(RMT) NOTIFICATION

This is for the information that, the students who are on roll in **III B.Tech II SEM** and **II B.Tech II SEM** who are absent for the I-Mid/II-Mid or both Mid examinations for various reasons are hereby informed to register for the **Remedial Mid Test (RMT)** in the prescribed format available in the examination branch. The last date for submission at exam branch is on or before **29th July 2022**. The issuing of application forms and registrations are done at exam branch.

1. The eligibility to appear for RMT is to have a valid reason with proof, duly certified by the respective HOD's.
2. The RMT contains Part-A and Part-B covering all five units of the syllabus. Part-A will have five questions of two marks each and all questions to be answered. Part-B will have six questions out of which four to be answered and each carries 10 marks. The duration of the examination is 2 hours.
3. Of the total 50 marks, the RMT will be adjusted for 25 marks and added to assignment and attendance marks of 5, thus making the total of RMT to 30 internal marks.
4. The previously scored Mid Exam Marks (Either in Mid-I/Mid-II) will be forfeited automatically as the student appears for the RMT and RMT marks will be considered as final.
5. If the Student is absent for RMT, the previously scored Mid Marks (Either in Mid-I/Mid-II) will remain the same.
6. The candidates have to register for RMT examination by paying Rs.1,000/- for one or two subjects and Rs.1,500/- for more than two subjects.

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DIRECTOR
(Academic Audit)

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PRINCIPAL

CONTROLLER OF EXAMINATIONS Sri Indu College of Engineering & Technology, Sheriguda, IBP, R.R. Dist-501510. (An Autonomous Institution under JNTUH)

Copy to:

All the respective HOD's & Coordinators, O., Account Section, Concerned Seat Clerk, SMS Incharge.



RMT TIME TABLE (OFF-LINE MODE)

II B.TECH - II SEMESTER - REMEDIAL MID TEST (RMT), JULY-2022.

(For the Admitted Batch 2020-21, BR-20 Regulation)

TIME OF EXAMINATION:- FN - 10:00 AM TO 12:00 PM and AN - 1:30 PM TO 3:30 PM

Sl.No	Branch	30.07.2022 (FN)	30.07.2022 (AN)	01.08.2022 (FN)	01.08.2022 (AN)	02.08.2022 (FN)
1	CIVIL	Basics of Electrical & Electronics Engineering R20EEE2204	Building Materials, Construction and Planning R20CIV2201	Strength of Materials-II R20CIV2202	Structural Analysis -I R20CIV2203	Engineering Geology R20CIV2204
2	EEE	Laplace Transforms , Numerical Methods & Complex variables R20MTH2201	Digital Logic Design R20ECE2102	Electrical Machines – II R20EEE2201	Control Systems R20EEE2202	Power System-I R20EEE2203
3	MECH	Basics of Electrical & Electronics Engineering R20EEE2204	Kinematics of Machinery R20MED2201	Thermal Engineering-I R20MED2202	Fluid Mechanics & Hydraulic Machines R20MED2203	Instrumentation & Control Systems R20MED2204
4	ECE	Laplace Transforms , Numerical Methods & Complex variables R20MTH2201	Electromagnetic Theory And Transmission Lines R20ECE2201	Analog and Digital Communications R20ECE2202	Linear and Digital IC Applications R20ECE2203	Electronic Circuit Analysis R20ECE2204
5	CSE	Discrete Mathematics R20CSE2201	Digital Logic Design R20ECE2102	Operating Systems R20CSE2202	Database Management Systems R20CSE2203	Java Programming R20CSE2204
6	IT	Discrete Mathematics R20CSE2201	Digital Logic Design R20ECE2102	Operating Systems R20CSE2202	Database Management Systems R20CSE2203	Java Programming R20CSE2204
7	CS	Discrete Mathematics R20CSE2201	Business Economics & Financial Analysis R20MBA2201	Operating Systems R20CSE2202	Fundamentals of Networking R20CSC2201	Java Programming R20CSE2204
8	AIML	Formal Language and Automata Theory R20CSE2206	Software Engineering R20CSE2207	Operating Systems R20CSE2202	Database Management Systems R20CSE2203	Java Programming R20CSE2204
9	DS	Formal Language and Automata Theory R20CSE2206	Software Engineering R20CSE2207	Operating Systems R20CSE2202	Database Management Systems R20CSE2203	Java Programming R20CSE2204
10	IoT	Computer Organization & Architecture R20CSE2102	Business Economics & Financial Analysis R20MBA2201	Operating Systems R20CSE2202	Fundamentals of Networking R20CSC2201	Java Programming R20CSE2204
11	CSIT	Discrete Mathematics R20CSE2201	Digital Logic Design R20ECE2102	Operating Systems R20CSE2202	Database Management Systems R20CSE2203	Java Programming R20CSE2204

NOTE:

- ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.
- IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED AFTER THE LAST DAY OF THE EXAMINATION.
- THE EXAMINATIONS WILL BE CONDUCTED OFF-LINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

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(For Admitted Batch 2019-20, BR-18 Regulation)

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No.	BRANCH	30.07.2022 (FN)	30.07.2022 (AN)	01.08.2022 (FN)	01.08.2022 (AN)	02.08.2022 (FN)	02.08.2022 (AN)
1	CIVIL	R18CIV3201 HYDROLOGY & WATER RESOURCES ENGINEERING	R18CIV3202 ENVIRONMENTAL ENGINEERING	R18MBA3201 ENGINEERING ECONOMICS & ACCOUNTING	R18CIV3203 STRUCTURAL ENGINEERING -II (STEEL)	R18CIV3221 PRESTRESSED CONCRETE STRUCTURES	R18CIV3271 DISASTER MANAGEMENT & MITIGATION
2	EEE	R18ECE2103 SIGNALS AND SYSTEMS	R18ECE3101 MICROPROCESSORS & MICROCONTROLLERS	R18EEE3201 POWER SYSTEM PROTECTION	R18EEE3202 POWER SYSTEM OPERATION AND CONTROL	R18EEE3221 POWER SEMICONDUCTOR DRIVES	R18EEE3274 ELECTRICAL ESTIMATION & COSTING
3	MECH	R18MED3201 DESIGN OF MACHINE MEMBERS-II	R18MED3202 HEAT TRANSFER	R18MED3203 CAD & CAM	R18MED3204 FINITE ELEMENT METHODS	R18MED3211 UNCONVENTIONAL MACHINING PROCESSES	R18CIV3271 DISASTER MANAGEMENT & MITIGATION
4	ECE	R18ECE3201 ANTENNAS AND WAVE PROPAGATION	R18ECE3202 DIGITAL SIGNAL PROCESSING	R18ECE3203 VLSI DESIGN	R18ECE3221 EMBEDDED SYSTEM DESIGN	R18ECE3273 CONSUMER ELECTRONICS	—
5	CSE	R18CSE3201 MACHINE LEARNING	R18CSE3202 COMPILER DESIGN	R18CSE3203 DESIGN AND ANALYSIS OF ALGORITHMS	R18CSE3231 SOFTWARE TESTING METHODOLOGIES	R18INF3275 INFORMATION TECHNOLOGY ESSENTIALS	—
6	IT	R18CSE3201 MACHINE LEARNING	R18INF3201 PRINCIPLES OF COMPILER CONSTRUCTION	R18INF3202 ALGORITHM DESIGN AND ANALYSIS	R18CSE3231 SOFTWARE TESTING METHODOLOGIES	R18INF3275 INFORMATION TECHNOLOGY ESSENTIALS	—

NOTE: (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

(ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAMINATION.

(iii) THE EXAMINATIONS WILL BE CONDUCTED OFFLINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

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