

# SRI INDU COLLEGE OF ENGINEERING AND TECHNOLOGY

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

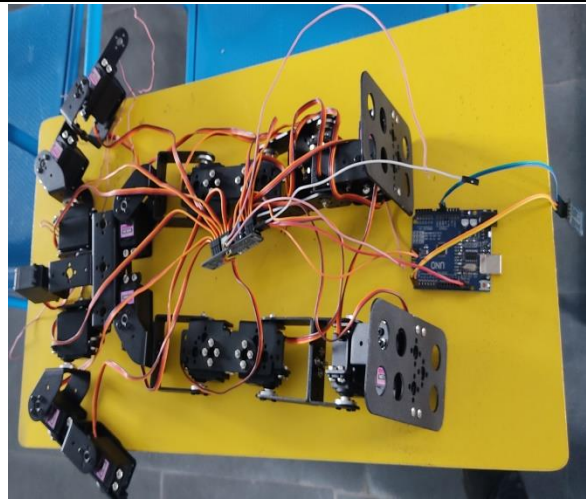
### Project laboratory

Sr. No	Name of the Facilities	Utilization
	MATLAB licensed version software and DSP kits in DSP Laboratory – 30 User	UG/PG students, Research Scholars and Faculty members utilize for their mini projects, projects, and research activities.
2	Keil micro vision 4 free version software tool and Microcontroller 8051 along with Inter facing kit in Microprocessors and Microcontrollers Laboratory	UG/PG students, Research Scholars and Faculty members utilize for their mini projects, projects, and research activities.
3	Antenna and Microwave components in Micro wave and Digital communication Lab and R&D Laboratory	UG/PG students, Research Scholars and Faculty members utilize for their mini projects, projects, and research activities.
4	VLSI – Spartan 3E FPGA along with Xilinx software and LT Spice software for designing and verifying codes of digital logic in ECAD-VLSI Laboratory.	UG/PG students, Research Scholars and Faculty members utilize for their mini projects, projects, and research activities.
5	Projects Development center	UG/PG students, Research Scholars and Faculty members utilize for their mini projects, projects, and research activities.

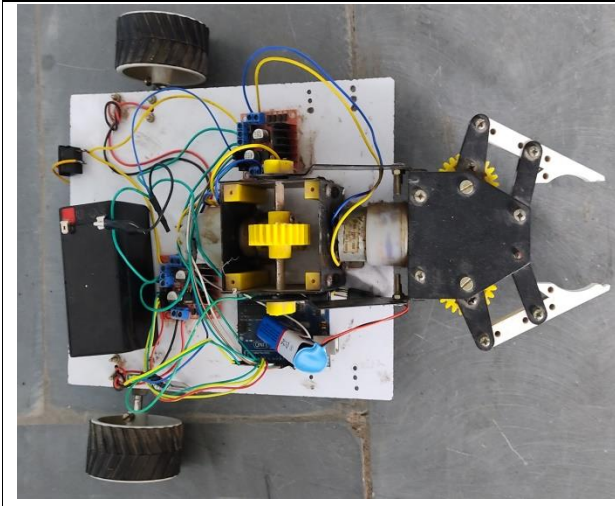
1. The department is equipped with project laboratory with an area of 66 Sq.m.
2. Project laboratory consists of well configured computer systems.
3. Systems are equipped with licensed versions of MATLAB, Xilinks version 9.0 and the students have free access to the lab during their project period.
4. All the systems in the laboratory are provided with the internet and the students can utilize the facility during their course of project work.
5. Student project models are displayed in the project laboratory



*Project Lab*



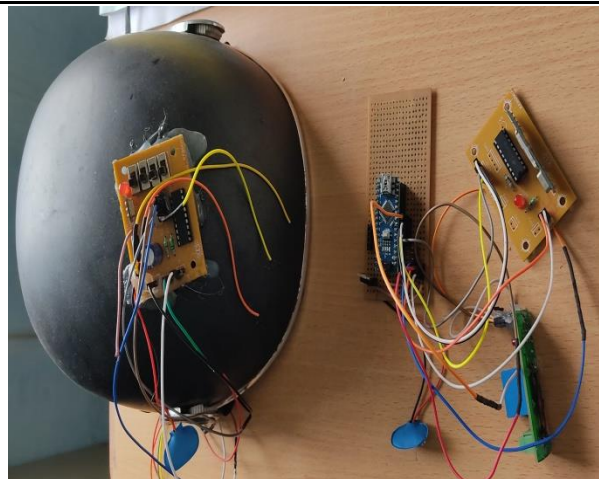
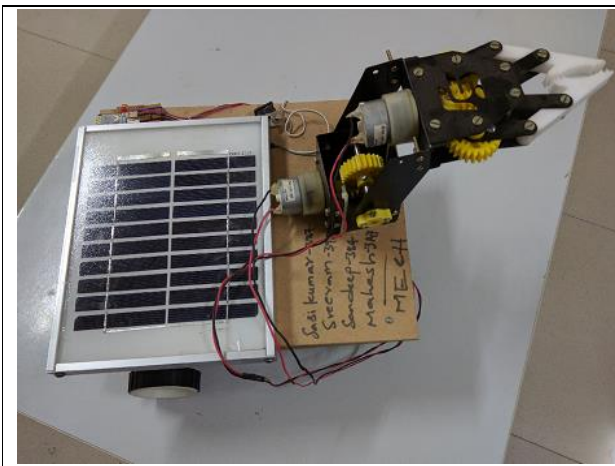
*Humanoid Robot*



*Pick & Place Robot*



*Home Automation*



*Solar Operated Robotic Arm*



*Smart Helmet*



*Rough train Vehicle*



*RC Plane*



*RC Plane*

*RC Plane*