



SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY
(An Autonomous Institution under UGC, New Delhi)
Recognized under 2(f) and 12(B) of UGC Act 1956
NBA Accredited, Approved by AICTE and Permanently affiliated to JNTUH
Sheriguda (V), Ibrahimpatnam, R.R. Dist, Hyderabad - 501 510

D4

MR-22

Time Table Issue Date: 03.03.2023

TIME TABLE (OFF-LINE MODE)

M.TECH : I YEAR - I SEMESTER (REGULAR) END EXAMINATION - MAR-2023

ADMITTED BATCH - 2022-23 (MR-22 REGULATION)

(BRANCH :- COMPUTER SCIENCE & ENGINEERING)

TIME OF EXAMINATION: FN - 10:00 AM TO 01:00 PM

SL.NO	DATE	SUBJECT
1	13.03.2023	Mathematical Foundations of Computer Science R22CSE1501
2	15.03.2023	Advanced Data Structures R22CSE1502
3	17.03.2023	Deep Learning R22CSE1504
4	20.03.2023	Software Quality Engineering R22CSE1507
5	23.03.2023	Research Methodology & IPR R22CSE1605

* [STUDENTS MUST AFFIX LATEST PHOTO WITH DATE PRINTED (Not taken earlier than a Month) ON HALL TICKET]

NOTE :

1. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.
2. IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED AFTER THE LAST DAY OF THE EXAMINATION.
3. THE EXAMINATIONS WILL BE CONDUCTED OFF-LINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.


ACE


CE 3/3/23


DIRECTOR


PRINCIPAL

CONTROLLER OF EXAMINATIONS
Sri Indu College of Engineering & Technology
(An Autonomous Institution under JNTUH)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.

DIRECTOR
(Academic Audit)
Sri Indu College of Engineering & Technology
Sheriguda, IBP, R.R. Dist-501510.

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MR-20

Time Table Issue Date: 03.03.2023

TIME TABLE (OFF-LINE MODE)

I M.TECH - I SEMESTER - (SUPPLEMENTARY) END EXAMINATION - MAR-2023

ADMITTED BATCHES: - 2021-22 and 2020-21 (SUPPL.)

TIME OF EXAMINATION: FN - 10:00 AM to 01:00 PM

SL.NO.	BRANCH	13.03.2023	15.03.2023	17.03.2023	20.03.2023	23.03.2023	25.03.2023
1	CSE	Advanced Data Structures and Algorithms (R20CSE1501)	Computer Networking (R20CSE1502)	Advanced Operating Systems (R20CSE1503)	Artificial Intelligence (R20CSE1504)	Software Process and Project Management (R20CSE1505)	Internet of Things (R20CSE1510)
2	ES	Micro Controllers And Programmable Digital Signal Processors (R20ES1501)	System Design With Embedded Linux (R20ES1502)	Programming Languages For Embedded Software (R20ES1503)	Communications Buses And Interfaces (R20ES1506)	Research Methodology And IPR (R20ES1509)	_____

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MR-18

Time Table Issue Date: 03.03.2023

TIME TABLE (OFF-LINE MODE)

M.TECH - I Year - I Semester - (SUPPLEMENTARY) END EXAMINATIONS, March-2023

(For Suppl. 2019-20 Batch - MR-18 Regulation)

(Common to COMPUTER SCIENCE & ENGINEERING & COMPUTER SCIENCE)

TIME OF EXAM:- FN - 10:00 AM to 01:00 PM

SL.NO.	DATE	SUBJECT
1	13.03.2023	Advanced Data Structures and Algorithms (R18CSE1501)
2	15.03.2023	Computer Networking (R18CSE1502)
3	17.03.2023	Advanced Operating Systems (R18CSE1503)
4	20.03.2023	Artificial Intelligence (R18CSE1504)
5	23.03.2023	Software Process and Project Management (R18CSE1505)
6	25.03.2023	Internet of Things (R18CSE1510)

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