



SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY
(An Autonomous Institution under UGC, New Delhi)
Recognized under 2(f) and 12(B) of UGC Act 1956
NBA Accredited, Approved by AICTE and Permanently affiliated to JNTUH
Sheriguda (V), Ibrahimpatnam, R.R. Dist, Hyderabad - 501 510

D4

MR-22

Time Table Issue Date: 19.02.2024

TIME TABLE (OFF-LINE MODE ONLY)

I M.TECH I SEMESTER - (REGULAR AND SUPPLEMENTARY) END EXAMINATION - MARCH-2024.

(ADMITTED BATCHS : 2023-24 (REGULAR) AND 2022-23 (SUPPL.), MR-22 Regulation.)

TIME OF EXAMINATION: FN - 10:00 AM to 01:00 PM

SL. NO.	BRANCH	04.03.2024	06.03.2024	09.03.2024	12.03.2024	14.03.2024
1	CSE	Mathematical Foundations of Computer Science R22CSE1501	Advanced Data Structures R22CSE1502	Deep Learning R22CSE1504	Software Quality Engineering R22CSE1507	Research Methodology & IPR R22CSE1605
2	ECE	Digital System Design with FPGAs R22ES1501	System Design with Embedded Linux R22ES1502	Wireless Sensor Networks R22ES1505	Communications Buses & Interfaces R22ES1506	Research Methodology & IPR R22ES1509

*** [STUDENTS MUST AFFIX LATEST PHOTO WITH DATE PRINTED (Not taken earlier than a Month) ON HALL TICKET]**

NOTE :

- (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.
- (ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED AFTER THE LAST DAY OF THE EXAMINATION.
- (iii) THE EXAMINATIONS WILL BE CONDUCTED OFF-LINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.


ACE


CE

Controller of Examination
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.


DIRECTOR

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Sri Indu College of Engineering & Technology
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Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.


PRINCIPAL
Sri Indu College of Engineering & Technology
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MR-20

Time Table Issue Date: 19.02.2024

TIME TABLE (OFF-LINE MODE)

I M.TECH - I SEMESTER - (SUPPLEMENTARY) END EXAMINATION - MAR-2024

ADMITTED BATCHES: - 2021-22 and 2020-21, MR-20 (SUPPL.)

TIME OF EXAMINATION: FN - 10:00 AM to 01:00 PM

SL.NO.	BRANCH	04.03.2024	06.03.2024	09.03.2024	12.03.2024	14.03.2024	16.03.2024
1	CSE	Advanced Data Structures and Algorithms (R20CSE1501)	Computer Networking (R20CSE1502)	Advanced Operating Systems (R20CSE1503)	Artificial Intelligence (R20CSE1504)	Software Process and Project Management (R20CSE1505)	Internet of Things (R20CSE1510)
2	ES	Micro Controllers And Programmable Digital Signal Processors (R20ES1501)	System Design With Embedded Linux (R20ES1502)	Programming Languages For Embedded Software (R20ES1503)	Communications Buses And Interfaces (R20ES1506)	Research Methodology And IPR (R20ES1509)	_____

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Time Table Issue Date: 17.02.2024

TIME TABLE (OFF-LINE MODE)

M.TECH : I YEAR - II SEMESTER (SUPPLEMENTARY) END EXAMINATION - MARCH-2024

ADMITTED BATCH - 2022-23 (MR-22 REGULATION)

(BRANCH :- COMPUTER SCIENCE & ENGINEERING)

TIME OF EXAMINATION: FN - 10:00 AM TO 01:00 PM

SL.NO	DATE	SUBJECT
1	05.03.2024	Advanced Algorithms R22CSE1509
2	07.03.2024	Advanced Computer Architecture R22CSE1510
3	11.03.2024	Advanced Computer Networks R22CSE1512
4	13.03.2024	Robotic Process Automation R22CSE1516

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I M.TECH - II SEMESTER - (SUPPLEMENTARY) END EXAMINATION - MARCH-2024

ADMITTED BATCHES - 2021-22 and 2020-21 (MR-20) REGULATION

TIME OF EXAMINATION: FN - 10:00 AM to 01:00 PM

S.No.	BRANCH	05.03.2024	07.03.2024	11.03.2024	13.03.2024	15.03.2024	18.03.2024
1	CSE	Network Programming (R20CSE1513)	Database System concepts (R20CSE1514)	Web Services and Service Oriented Architecture (R20CSE1515)	Big Data Analytics (R20CSE1516)	Android Application Development (R20CSE1519)	Distributed Systems and Cloud Computing (R20CSE1523)
2	ES	RTL Simulation and Synthesis with PLDs (R20ES1510)	Advanced Digital Signal Processing (R20ES1511)	IOT and its Applications (R20ES1512)	Hardware and Software Co-Design (R20ES1515)	_____	_____

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