SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY
(An Autonomous Institution under UGC, New Delhi)
Recognized under 2(f) and 12(B) of UGC Act 1956
NBA Accredited, Approved by AICTE and Permanently affiliated to JNTUH
Sheriguda (V), Ibrahimpatnam, R.R.Dist, Hyderabad - 501 510

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MR-22

Time Table Issue Date: 18.05.2024

TIME TABLE (OFF-LINE MODE ONLY)

I M.TECH - II SEMESTER - I MID TERM EXAMINATIONS, MAY - 2024.

ADMITTED BATCH : 2023-24, MR-22 Regulation.

TIME OF EXAMINATION:- FN - 10:00 AM TO 12:00 PM

SL. NO.	BRANCH	27-05-2024	28-05-2024	29-05-2024	30-05-2024
1	CSE	Advanced Algorithms R22CSE1509	Advanced Computer Architecture R22CSE1510	Advanced Computer Networks R22CSE1512	Robotic Process Automation R22CSE1516
2	ECE	ARM Microcontrollers R22ES1510	Digital Control Systems R22ES1511	IoT Architectures and System Design R22ES1512	Hardware and Software Co-Design R22ES1515

NOTE: i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAM.

iii) THE EXAMINATIONS WILL BE CONDUCTED OFFLINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

ACE

CE
Controller of Examination
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510

DIRECTOR
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.

PRINCIPAL
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.