SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY

(An Autonomous Institution under UGC, New Delhi)
Recognized under 2(f) and 12(B) of UGC Act 1956
NBA Accredited, Approved by AICTE and Permanently attiliated to JNTUH
Sheriguda (V), Ibrahimpatnam, R.R.Dist, Hyderabad - 501 510

Time Table Issue Date: 31.07.2024

TIME TABLE (OFF-LINE MODE ONLY)
I M.TECH - II SEMESTER REGULAR END EXAMINATIONS, AUG - 2024.
ADMITTED BATCH : 2023-24, MR-22 Regulation.

TIME OF EXAMINATION:- FN - 10:00 AM TO 01: 00 PM

SL. NO.	BRANCH	12-08-2024	14-08-2024	17-08-2024	20-08-2024
1	CSE	Advanced Algorithms R22CSE1509	Advanced Computer Architecture R22CSE1510	Advanced Computer Networks R22CSE1512	Robotic Process Automation R22CSE1516
2	ES	ARM Microcontrollers R22ES1510	Digital Control Systems R22ES1511	IoT Architectures and System Design R22ES1512	Hardware and Software Co-Design R22ES1515

NOTE: i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAM.

iii) THE EXAMINATIONS WILL BE CONDUCTED OFFLINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

ACE

CE
Controller of Examination
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510

DIRECTOR
DIRECTOR
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.

PRINCIPAL
PRINCIPAL
Sri Indu College of Engineering & Technology
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| D4 |
| MR-22 |

Time Table Issue Date: 31.07.2024

TIME TABLE (OFF-LINE MODE)
M.TECH : I YEAR - II SEMESTER (SUPPLY) END EXAMINATIONS - AUG-2024
ADMITTED BATCH - 2022-23 (MR-22 REGULATION)
(BRANCH :- COMPUTER SCIENCE & ENGINEERING)

TIME OF EXAMINATION: FN - 10:00 AM TO 01:00 PM

SL.NO	DATE	SUBJECT
1	12.08.2024	**Advanced Algorithms** **R22CSE1509**
2	14.08.2024	**Advanced Computer Architecture** **R22CSE1510**
3	17.08.2024	**Advanced Computer Networks** **R22CSE1512**
4	20.08.2024	**Robotic Process Automation** **R22CSE1516**

* [STUDENTS MUST AFFIX LATEST PHOTO WITH DATE PRINTED (Not taken earlier than a Month) ON THE HALL TICKET]

NOTE :

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2. IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED AFTER THE LAST DAY OF THE EXAMINATION.

3. THE EXAMINATIONS WILL BE CONDUCTED OFF-LINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

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D4

MR-20

Time Table Issue Date: 31.08.2024

TIME TABLE (OFF-LINE MODE)
I M.TECH - II SEMESTER - (SUPPLEMENTARY) END EXAMINATIONS - AUG-2024
FOR ADMITTED BATCHES: 2021-22 and 2020-21

TIME OF EXAMINATION: FN - 10:00 AM to 01:00 PM

SL.NO.	BRANCH	12.08.2024	14.08.2024	17.08.2024	20.08.2024	22.08.2024	24.08.2024
1	CSE	Network Programming (R20CSE1513)	Database System concepts (R20CSE1514)	Web Services and Service Oriented Architecture (R20CSE1515)	Big Data Analytics (R20CSE1516)	Android Application Development (R20CSE1519)	Distributed Systems and Cloud Computing (R20CSE1523)
2	ES	RTL Simulation and Synthesis with PLDs (R20ES1510)	Advanced Digital Signal Processing (R20ES1511)	IOT and its Applications (R20ES1512)	Hardware and Software Co-Design (R20ES1515)	————	————

* [STUDENTS MUST AFFIX LATEST PHOTO WITH DATE PRINTED (Not taken earlier than a Month) ON HALL TICKET]

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(iii) THE EXAMINATIONS WILL BE CONDUCTED OFF-LINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

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