



**Time Table Issue Date: 31.07.2024**

**TIME TABLE (OFF-LINE MODE ONLY)**

**I M.TECH I SEMESTER - ( SUPPLEMENTARY) END EXAMINATIONS - AUG 2024.**

**(ADMITTED BATCHES : 2023-24 AND 2022-23 (SUPPL.), MR-22 Regulation.)**

**TIME OF EXAMINATION: FN - 10:00 AM to 01:00 PM**

SL. NO.	BRANCH	13.08.2024	16.08.2024	19.08.2024	21.08.2024	23.08.2024
1	CSE	Mathematical Foundations of Computer Science R22CSE1501	Advanced Data Structures R22CSE1502	Deep Learning R22CSE1504	Software Quality Engineering R22CSE1507	Research Methodology & IPR R22CSE1605
2	ES	Digital System Design with FPGAs R22ES1501	System Design with Embedded Linux R22ES1502	Wireless Sensor Networks R22ES1505	Communications Buses & Interfaces R22ES1506	Research Methodology & IPR R22ES1509

**\* [STUDENTS MUST AFFIX LATEST PHOTO WITH DATE PRINTED (Not taken earlier than a Month) ON HALL TICKET]**

**NOTE :**

- (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.
- (ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED AFTER THE LAST DAY OF THE EXAMINATION.
- (iii) THE EXAMINATIONS WILL BE CONDUCTED OFF-LINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

*Handwritten signatures:*  
ACE  
MSE  
DRB

*Signature:*  
Controller of Examination  
Sri Indu College of Engineering & Technology  
(An Autonomous Institution Under UGC)  
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.

*Signature:*  
DIRECTOR  
Sri Indu College of Engineering & Technology  
(An Autonomous Institution Under UGC)  
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.

*Signature:*  
PRINCIPAL  
PRINCIPAL  
Sri Indu College of Engineering & Technology  
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Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.



**SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY**  
(An Autonomous Institution under UGC, New Delhi)  
Recognized under 2(f) and 12(B) of UGC Act 1956  
NBA Accredited. Approved by AICTE and Permanently affiliated to INTUH  
Sheriguda (V), Ibrahimpatnam, R.R. Dist. Hyderabad - 501 510

D4

MR-20

Time Table Issue Date: 31.07.2024

## TIME TABLE (OFF-LINE MODE)

**I M.TECH - I SEMESTER - (SUPPLEMENTARY) END EXAMINATIONS - AUG-2024**

**ADMITTED BATCHES: - 2021-22 and 2020-21, MR-20 (SUPPL.)**

**TIME OF EXAMINATION: FN - 10:00 AM to 01:00 PM**

SL.NO.	BRANCH	13.08.2024	16.08.2024	19.08.2024	21.08.2024	23.08.2024	27.08.2024
1	CSE	Advanced Data Structures and Algorithms (R20CSE1501)	Computer Networking (R20CSE1502)	Advanced Operating Systems (R20CSE1503)	Artificial Intelligence (R20CSE1504)	Software Process and Project Management (R20CSE1505)	Internet of Things (R20CSE1510)
2	ES	Micro Controllers And Programmable Digital Signal Processors (R20ES1501)	System Design With Embedded Linux (R20ES1502)	Programming Languages For Embedded Software (R20ES1503)	Communications Buses And Interfaces (R20ES1506)	Research Methodology And IPR (R20ES1509)	_____

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