

SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY

(An Autonomous Institution under UGC, New Delhi)

Recognized under 2(f) and 12(B) of UGC Act 1956

NBA Accredited, Approved by AICTE and Permanently affiliated to INTUH Sheriguda (V), Ibrahimpatnam, R.R.Dist, Hyderabad - 501 510

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Time Table Issue Date: 28.11.2024

## TIME TABLE (OFF-LINE MODE ONLY)

IM.TECH - I SEMESTER - I MID TERM EXAMINATIONS, DECEMBER - 2024.

ADMITTED BATCH: 2024-25, MR-22 Regulation.

## TIME OF EXAMINATION:- FN - 10:00 AM TO 12:00 PM

SL. NO.	BRANCH	09.12.2024	10.12.2024	11.12.2024	12.12.2024	13.12.2024
1	CSE	Mathematical Foundations of Computer Science R22CSE1501	Advanced Data Structures R22CSE1502	Deep Learning R22CSE1504	Software Quality Engineering R22CSE1507	Research Methodology & IPR R22CSE1605
2	ES	Digital System Design with FPGAs R22ES1501	System Design with Embedded Linux R22ES1502	Wireless Sensor Networks R22ES1505	Communications Buses & Interfaces R22ES1506	Research Methodology & IPR R22ES1509

NOTE: i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY..

ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAM.

iii) THE EXAMINATIONS WILL BE CONDUCTED OFFLINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.

(An Autonomous Institution Under UGC) Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510

DIKECTUK Sri Indu College of Engineering & Technology (An Autonomous Institution Under UGC) Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510

DIRECTOR

PRINCIPAL (An Autonomous Institution Under UGC) Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.