



**MODIFIED EXAMINATION TIME TABLE (OFF- LINE MODE)**

**III B.TECH I SEMESTER, II-MID TERM EXAMINATIONS, DECEMBER - 2025**

**Admitted Batch : 2023-24**

**Regulation: BR-22**

**Duration:2 Hours**

**Sessions - Fore noon - 10.00 AM TO 12.00 Noon and Afternoon - 1.30 PM TO 3.30 PM**

| Sl.No | Branch                    | 15.12.2025 (FN)   | 15.12.2025 (AN)  | 16.12.2025 (FN)  | 16.12.2025 (AN)  | 19.12.2025 (FN)  | 19.12.2025 (AN)<br>Time:01.30 pm to 04:30 pm | 20.12.2025(FN)                                    |
|-------|---------------------------|---|--|--|--|--|--|---|
| 1     | CIVIL                     | Structural Analysis - II<br>(R22CIV3111)                | Geotechnical Engineering<br>(R22CIV3112)                     | Structural Engineering -I<br>(RCC)<br>(R22CIV3113)                         | Transportation Engineering<br>(R22CIV3115)                   | Business Economics &<br>Financial Analysis<br>(R22HMS1212) | Intellectual Property Rights<br>(R22MAC3110) | Water Resources<br>Engineering - I<br>R22CIV3116) |
| 2     | EEE                       | Control Systems<br>(R22EEE3113)                         | Power Electronics<br>(R22EEE3111)                            | Microprocessors &<br>Microcontroller<br>Peripheral Devices<br>(R22EEE3112) | High Voltage Engineering<br>(R22EEE3144)                     | Business Economics &<br>Financial Analysis<br>(R22HMS1212) | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 3     | ECE                       | Control Systems<br>(R22EEE3113)                         | Data Communications and<br>Networks<br>(R22INF3145)          | Microprocessors &<br>Microcontrollers<br>(R22ECE3111)                      | Computer Organization &<br>Operating Systems<br>(R22CSE3141) | Business Economics &<br>Financial Analysis<br>(R22HMS1212) | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 4     | CSE                       | Design and Analysis of<br>Algorithms<br>(R22CSE3111)    | Computer Networks<br>(R22CSE3112)                            | DevOps<br>(R22CSE3113)   | Principles of Programming<br>Languages<br>(R22CSE3143)       | Distributed Databases<br>(R22CSE3146)                      | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 5     | IT                        | Software Engineering<br>(R22CSE2215)                    | Data Communications and<br>Computer Networks<br>(R22INF3112) | Machine Learning<br>(R22CSM3112)   | Principles of Programming<br>Languages<br>(R22CSE3143)       | Distributed Databases<br>(R22CSE3146)                      | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 6     | CSIT                      | Formal Languages and<br>Automata Theory<br>(R22CSE3212) | Data Communications and<br>Computer Networks<br>(R22INF3112) | Machine Learning<br>(R22CSM3112)   | Principles of Programming<br>Languages<br>(R22CSE3143)       | Distributed Databases<br>(R22CSE3146)                      | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 7     | CSE<br>(AI&ML)            | Design and Analysis of<br>Algorithms<br>(R22CSE3111)    | Computer Networks<br>(R22CSE3112)                            | Machine Learning<br>(R22CSM3112)   | Web Programming<br>(R22CSD3146)                              | Business Economics &<br>Financial Analysis<br>(R22HMS1212) | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 8     | AI&DS                     | Design and Analysis of<br>Algorithms<br>(R22CSE3111)    | Computer Networks<br>(R22CSE3112)                            | Data Science<br>(R22CSD3145)   | Web Programming<br>(R22CSD3146)                              | Business Economics &<br>Financial Analysis<br>(R22HMS1212) | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 9     | CSE<br>(DATA<br>SCIENCE)  | Algorithm Design and<br>Analysis<br>(R22INF3212)        | Computer Networks<br>(R22CSE3112)                            | Introduction to Data Science<br>(R22CSD3112)                               | Artificial Intelligence<br>(R22CSM4143)                      | Software Project<br>Management<br>(R22CSD3147)             | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 10    | CSE<br>(CYBER<br>SECURIT) | Formal Languages and<br>Automata Theory<br>(R22CSE3212) | Database Management<br>Systems<br>(R22CSE2214)               | Network Security and<br>Cryptography<br>(R22CSC3111)                       | Cloud Computing<br>(R22CSE4143)                              | Ethical Hacking<br>(R22CSC3145)                            | Intellectual Property Rights<br>(R22MAC3110) | _____   |
| 11    | CSE<br>(IoT)              | Automata Theory and<br>Compiler Design<br>(R22CSM2212)  | Database Management<br>Systems<br>(R22CSE2214)               | Microprocessors &<br>Microcontrollers<br>(R22ECE3111)                      | IoT System Architectures<br>(R22CSO3145)                     | Business Economics &<br>Financial Analysis<br>(R22HMS1212) | Intellectual Property Rights<br>(R22MAC3110) | _____   |

NOTE: i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

(ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAMINATION.

(iii) THE EXAMINATIONS WILL BE CONDUCTED OFFLINE. ALL THE STUDENTS MUST ATTEND TO THE COLLEGE AND APPEAR FOR THE EXAMINATIONS.

*ACE*

**Controller of Examination**  
Sri Indu College of Engineering & Technology  
(An Autonomous Institution Under UGC)  
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.

*for Director*  
**DIRECTOR**  
Sri Indu College of Engineering & Technology  
(An Autonomous Institution Under UGC)  
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.

*Principal*  
**PRINCIPAL**  
Sri Indu College of Engineering & Technology  
(An Autonomous Institution Under UGC)  
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.