SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY

(An Autonomous Institution under UGC, New Delhi)
Recognized under 2(f) and 12(B) of UGC Act 1956
NBA & NAAC Accredited, Approved by AICTE and Permanently affiliated to JNTUH
Sheriguda (V), Ibrahimpatnam, R.R.Dist, Hyderabad - 501 510

D4

BR- 22

Time Table Issue Date:22.11.2025

EXAMINATION TIME TABLE (OFF- LINE MODE)
III B.TECH I SEMESTER, II-MID TERM EXAMINATIONS, DECEMBER - 2025
Admitted Batch : 2023-24

Regulation: BR-22

Duration:2 Hours

Sessions - Fore noon - 10.00 AM TO 12.00 Noon and Afternoon - 1.30 PM TO 3.30 PM

Sl.No	Branch	15.12.2025 (FN)	15.12.2025 (AN)	16.12.2025 (FN)	16.12.2025 (AN)	17.12.2025 (FN)	17.12.2025 (AN) Time:1.30 pm to 4:30 pm	18.12.2025 (FN)
1	CIVIL	Structural Analysis - II (R22CIV3111)	Geotechnical Engineering (R22CIV3112)	Structural Engineering -I (RCC) (R22CIV3113)	Transportation Engineering (R22CIV3115)	Business Economics & Financial Analysis (R22HMS1212)	Intellectual Property Rights (R22MAC3110)	Water Resources Engineering - I R22CIV3116)
2	EEE	Control Systems (R22EEE3113)	Power Electronics (R22EEE3111)	Microprocessors & Microcontroller Peripheral Devices (R22EEE3112)	High Voltage Engineering (R22EEE3144)	Business Economics & Financial Analysis (R22HMS1212)	Intellectual Property Rights (R22MAC3110)	_____
3	ECE	Control Systems (R22EEE3113)	Data Communications and Networks (R22INF3145)	Microprocessors & Microcontrollers (R22ECE3111)	Computer Organization & Operating Systems (R22CSE3141)	Business Economics & Financial Analysis (R22HMS1212)	Intellectual Property Rights (R22MAC3110)	_____
4	CSE	Design and Analysis of Algorithms (R22CSE3111)	Computer Networks (R22CSE3112)	DevOps (R22CSE3113)	Principles of Programming Languages (R22CSE3143)	Distributed Databases (R22CSE3146)	Intellectual Property Rights (R22MAC3110)	_____
5	IT	Software Engineering (R22CSE2215)	Data Communications and Computer Networks (R22INF3112)	Machine Learning (R22CSM3112)	Principles of Programming Languages (R22CSE3143)	Distributed Databases (R22CSE3146)	Intellectual Property Rights (R22MAC3110)	_____
6	CSIT	Formal Languages and Automata Theory (R22CSE3212)	Data Communications and Computer Networks (R22INF3112)	Machine Learning (R22CSM3112)	Principles of Programming Languages (R22CSE3143)	Distributed Databases (R22CSE3146)	Intellectual Property Rights (R22MAC3110)	_____
7	CSE (AI&ML)	Design and Analysis of Algorithms (R22CSE3111)	Computer Networks (R22CSE3112)	Machine Learning (R22CSM3112)	Web Programming (R22CSD3146)	Business Economics & Financial Analysis (R22HMS1212)	Intellectual Property Rights (R22MAC3110)	_____
8	AI&DS	Design and Analysis of Algorithms (R22CSE3111)	Computer Networks (R22CSE3112)	Data Science (R22CSD3145)	Web Programming (R22CSD3146)	Business Economics & Financial Analysis (R22HMS1212)	Intellectual Property Rights (R22MAC3110)	_____
9	CSE (DATA SCIENCE	Algorithm Design and Analysis (R22INF3212)	Computer Networks (R22CSE3112)	Introduction to Data Science (R22CSD3112)	Artificial Intelligence (R22CSM4143)	Software Project Management (R22CSD3147)	Intellectual Property Rights (R22MAC3110)	_____
10	CSE (CYBER SECURIT	Formal Languages and Automata Theory (R22CSE3212)	Database Management Systems (R22CSE2214)	Network Security and Cryptography (R22CSC3111)	Cloud Computing (R22CSE4143)	Ethical Hacking (R22CSC3145)	Intellectual Property Rights (R22MAC3110)	_____
11	CSE (IoT)	Automata Theory and Compiler Design (R22CSM2212)	Database Management Systems (R22CSE2214)	Microprocessors & Microcontrollers (R22ECE3111)	IoT System Architectures (R22CSO3145)	Business Economics & Financial Analysis (R22HMS1212)	Intellectual Property Rights (R22MAC3110)	_____

NOTE: i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY.

(ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAMINATION

iii) THE EXAMINATIONS WILL BE CONDUCTED OFFLINE. ALL THE STUDENTS MUST ATTEND TO THE COLLEGE AND APPEAR FOR THE EXAMINATIONS.

ACE

Controller of Examination
CE
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.

DIRECTOR
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510

PRINCIPAL
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.