



SRI INDU COLLEGE OF ENGINEERING & TECHNOLOGY
(An Autonomous Institution under UGC, New Delhi)
Recognized under 2(f) and 12(B) of UGC Act 1956
NBA & NAAC Accredited, Approved by AICTE and Permanently affiliated to JNTUH
Sheriguda (V), Ibrahimpatnam, R.R. Dist, Hyderabad - 501 510

D4

MR-25

Time Table Issue Date: 17.08.2026

EXAMINATION TIME TABLE (OFF-LINE MODE)
I M.TECH - II SEMESTER REGULAR END EXAMINATIONS, AUG/SEP- 2026.
ADMITTED BATCH : 2025-26, MR-25 Regulation.

Session: Fore Noon 10:00 AM To 01:00 PM					
Duration: 3 Hours					
SL. NO.	BRANCH	31.08.2026	02.09.2026	05.09.2026	08.09.2026
1	CSE	Advanced Algorithms (R25CSE1201)	Advanced Computer Architecture (R25CSE1202)	Parallel computing (R25CSE1205)	Generative AI (R25CSE1211)
2	ES	Embedded Real Time Operating Systems (R25ES1201)	IoT System Design (R25ES1202)	Hardware Software Co-Design (R25ES1205)	Hardware Security in VLSI Design (R25ES1206)

NOTE: i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE DEAN OF EXAMINATIONS IMMEDIATELY..
ii) IN CASE THE GOVERNMENT DECLARES A HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATION SCHEDULED ON THAT DAY WILL BE CONDUCTED ONE DAY AFTER THE LAST DAY OF THE EXAM.
iii) THE EXAMINATIONS WILL BE CONDUCTED OFFLINE. ALL THE STUDENTS MUST ATTEND TO COLLEGE AND APPEAR FOR THE EXAMINATIONS.


ACE


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Controller of Examinations
Sri Indu College of Engineering & Technology
(An Autonomous Institution Under UGC)
Sheriguda (V), Ibrahimpatnam, R.R. Dist-501510.


DIRECTOR
Sri Indu College of Engineering & Technology
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PRINCIPAL
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Time Table Issue Date: 17.08.2026

EXAMINATION TIME TABLE (OFF-LINE MODE)

I M.TECH - II SEMESTER REGULAR END EXAMINATIONS , SEP- 2026.

ADMITTED BATCH : 2025-26, MR-25 Regulation.

Program: Ph.D Course work			
Duration: 3 Hours		Session: Fore Noon 10:00 AM To 01:00 PM	
SL. NO.	BRANCH	05.09.2026	08.09.2026
1	CSE	Large Language Models (R25CSE1206) Hall Ticket No: 2301305002	Generative AI (R25CSE1211) Hall Ticket No: 2301305002

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MR-22

Time Table Issue Date: 17.08.2026

EXAMINATIONS TIME TABLE (OFF-LINE MODE)

I M.TECH - II SEMESTER SUPPLY END EXAMINATIONS , AUG/- 2026.

ADMITTED BATCHES : SUPPLY : 2022-23,2023-24 & 2024-25

Regulation: MR-22

TIME: 10:00 AM TO 01:00 PM

Session: FN

SL. NO.	BRANCH	31-08-2026	02-09-2026	05-09-2026	08-09-2026
1	CSE	Advanced Algorithms R22CSE1509	Advanced Computer Architecture R22CSE1510	Advanced Computer Networks R22CSE1512	Robotic Process Automation R22CSE1516
2	ECE	ARM Microcontrollers R22ES1510	Digital Control Systems R22ES1511	IoT Architectures and System Design R22ES1512	Hardware and Software Co-Design R22ES1515

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EXAMINATION TIME TABLE (OFF-LINE MODE)

I M.TECH I SEMESTER SUPPLY END EXAMINATIONS, SEP - 2026.

ADMITTED BATCH : 2025-26, MR-25 Regulation.

Duration: 3 Hours

Session: Fore noon 10:00 AM To 01:00 PM

SL. NO.	BRANCH	01.09.2026	03.09.2026	07.09.2026	09.09.2026	10.09.2026
1	CSE	Mathematical Foundations of Computer Science (R25CSE1110)	Advanced Data Structures (R25CSE1101)	Deep Learning (R25CSE1103)	Software Quality Engineering (R25CSE1107)	Research Methodology & IPR (R25VAC1101)
2	ES	FPGA Based System Design (R25ES1101)	Embedded System Design (R25ES1102)	Wireless Sensor Networks (R25ES1104)	Advanced RISC Architectures (R25ES1106)	Research Methodology & IPR (R25VAC1101)

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[Signature]
ACE

[Signature] 17/8/26
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MR-22

Time Table Issue Date: 17.08.2026

TIME TABLE (OFF-LINE MODE ONLY)

I M.TECH - I SEMESTER SUPPLY END EXAMINATIONS, MARCH - 2026.

SUPPLY BATCHES : 2024-25 , 2023-24 & 2022-23 MR-22 Regulation.

TIME OF EXAMINATION:- FN - 10:00 AM TO 1:00 PM

SL. NO.	BRANCH	01.09.2026	03.09.2026	07.09.2026	09.09.2026	10.09.2026
1	CSE	Mathematical Foundations of Computer Science R22CSE1501	Advanced Data Structures R22CSE1502	Deep Learning R22CSE1504	Software Quality Engineering R22CSE1507	Research Methodology & IPR R22CSE1605
2	ES	Digital System Design with FPGAs R22ES1501	System Design with Embedded Linux R22ES1502	Wireless Sensor Networks R22ES1505	Communications Buses & Interfaces R22ES1506	Research Methodology & IPR R22ES1509

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